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**Form PTO-1449** (modified)

List of Patents and Publications for Applicant's

**INFORMATION DISCLOSURE STATEMENT**

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Atty. Docket No.  
SURG:158Serial No.  
UnknownApplicant  
Scott Johnson et al.Filing Date:  
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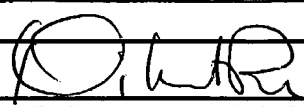
Group: Unknown

PC868 U.S. PTO  
09/871134  
05/31/01

U.S. Patent Documents  
See Page 1Foreign Patent Documents  
See Page 2Other Art  
See Pages 2-4**U.S. Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
NA	A1	5,592,672	1/7/97	Grewal et al.			1/18/96
	A2	5,408,465	4/18/95	Gusella et al.			6/21/93
	A3	6,031,841	2/29/00	Woundy			12/23/97
	A4	6,002,667	12/14/99	Manning et al.			7/18/96
	A5	5,996,013	11/30/99	Delp et al.			4/30/97
	A6	5,982,771	11/9/99	Caldara et al.			7/18/96
	A7	5,941,951	8/24/99	Day et al.			10/31/97
	A8	5,940,372	8/17/99	Bertin et al.			7/12/96
	A9	6,091,725	7/18/00	Cheriton et al.			12/29/95
	A10	5,307,347	4/26/94	Duault et al.			4/10/92
	A11	5,490,258	2/6/96	Fenner			9/29/92
	A12	6,034,958	3/7/00	Wicklund			7/11/97
	A13	6,067,574	5/23/00	Tzeng			5/18/98
	A14	5,815,662	9/29/98	Ong			8/15/96
	A15	5,991,306	11/23/99	Burns et al.			8/26/96
	A16	5,987,611	11/16/99	Freund			5/6/97
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	A18	5,809,258	9/15/98	Bermanian et al.			1/6/97
	A19	5,334,962	8/2/94	Higgins et al.			5/29/90
	A20	5,941,969	8/24/99	Ram et al.			10/22/97
	A21	5,987,627	11/16/99	Rawlings, III			10/27/92
	A22	4,716,525	12/29/87	Gilanyi et al.			4/15/85
	A23	4,276,597	6/30/81	Dissly et al.			1/17/74

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Date Considered:

7/20/04

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U.S. Patent Documents

See Page 1

Foreign Patent Documents

See Page 2

Other Art

See Pages 2-4

**Foreign Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
W	B1	WO 99/33229	7/1/99				Yes
J	B2	WO 99/03298	1/21/99				Yes
Y	B3	WO 97/47113	12/11/97				Yes

**Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)**

Exam. Init.	Ref. Des.	Citation
W	C1	Copending U.S. Patent Application Serial No. 09/797,414, filed March 1, 2001, "Network Content Delivery System With Peer To Peer Processing Components"
	C2	Copending U.S. Patent Application Serial No. 09/797,411, Filed March 1, 2001, "Network Security Accelerator"
	C3	Copending U.S. Patent Application Serial No. 09/797,198, Filed March 1, 2001, "Systems And Methods For Management of Memory"
	C4	Copending U.S. Patent Application Serial No. 09/797,413, Filed March 1, 2001, "Network Connected Computing System"
	C5	Copending U.S. Patent Application Serial No. 09/797,199, Filed March 1, 2001, "System And Apparatus For Accelerating Content Delivery Throughout Networks"
	C6	Copending U.S. Application Serial No. 09/797,200, Filed March 1, 2001, "Systems And Methods For The Deterministic Management Of Information"
	C7	Copending U.S. Application Serial No. 09/797,197, Filed March 1, 2001, "Method And Systems For The Order Serialization Of Information In A Network Processing Environment"
	C8	Copending U.S. Application Serial No. 09/797,404, Filed March 1, 2001, "Interprocess Communications Within A Network Node Using Switch Fabric"
	C9	Copending U.S. Application Serial No. 09/797,507, Filed March 1, 2001, "Single Chassis Network Endpoint System With Network Processor For Load Balancing"

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U.S. Patent Documents

See Page 1

Foreign Patent Documents

See Page 2

Other Art

See Pages 2-4

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Exam. Init.	Ref. Des.	Citation
<i>MP</i>	C10	Copending U.S. Application Serial No. 09/797,201, Filed March 1, 2001, "Systems And Methods For Management Of Memory In Information Delivery Environments"
	C11	Copending U.S. Application Serial No. 09/797,412, Filed March 1, 2001, "Network Transport Accelerator"
	C12	Copending U.S. Application Serial No. 09/797,506, Filed March 1, 2001, "Network Endpoint System With Accelerated Data Path"
	C13	Copending U.S. Application Serial No. 09/797,195, Filed March 1, 2001, "Content Delivery System Providing Accelerate Content Delivery"
	C14	"Network Processor Designs for Next-Generation Networking Equipment White Paper" <a href="http://www.exchip.com/html/tech_nsppaper.html">http://www.exchip.com/html/tech_nsppaper.html</a> , 8/30/00, pgs. 1-8
	C15	"C5DCP: C-5 Digital Communications Processor (DCP)" <a href="http://ebus.mot-sps.com/ProdCat/psp/0,1250,C5DCP-M957198845249,00.html">http://ebus.mot-sps.com/ProdCat/psp/0,1250,C5DCP-M957198845249,00.html</a> , 9/11/00, pgs. 1-5
	C16	Product Brief, C-Port A Motorola Company, "C-5 Digital Communications Processor" pgs. 1-8
	C17	Product Brief, Prism IQ2000, Family of Network Processors, "Ushering In A new Era Of Performance and Flexibility, pgs. 1-4
	C18	"Strategies to Protect Against Distributed Denial of Service (DdoS) Attacks" <a href="http://www.ieng.com/warp/public/707/newflash.html">http://www.ieng.com/warp/public/707/newflash.html</a> , 2/17/00, pgs. 1-6
	C19	Press Release, Vitesse and Power X Announce Agreement To Provide Solutions For High-Performance Networking Applications, <a href="http://www.sitera.com/news_events/pr_archive/partner_pr/pr090999.html">http://www.sitera.com/news_events/pr_archive/partner_pr/pr090999.html</a> , 9/19/00, pgs 1-3
	C20	Press Release, Power X Unveils Switch Fabric Breakthrough At Networkd & Interop In Atlanta, <a href="http://www.px.uk.com/px_news_press_10.htm">http://www.px.uk.com/px_news_press_10.htm</a> , 9/19/00, pgs. 1-4
	C21	"SP2 High Performance Switch Overview" <a href="http://www.mhpcc.edu/training/workshop/ibmhsw/switch.html">http://www.mhpcc.edu/training/workshop/ibmhsw/switch.html</a> , 9/19/00, pgs. 1-4
	C22	CSIX Position Paper, "The Need For A New Common Switch/Fabric Interface To Support Next Generation Networks" <a href="http://www.csix.org/csixpp.htm">http://www.csix.org/csixpp.htm</a> , 9/19/00, pgs. 1-4
	C23	Press Release, C-Port and Power X Announce Agreement To Deliver Integrated Solution For High-Performance Networking Applications" <a href="http://www.cportcorp.com/news/pr_990329.htm">http://www.cportcorp.com/news/pr_990329.htm</a> , 9/19/00, pgs. 1-3

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U.S. Patent Documents

See Page 1

Foreign Patent Documents

See Page 2

Other Art

See Pages 2-4

**Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)**

Exam. Init.	Ref. Des.	Citation
NP	C24	"ASP Server Farm Load Balancing" <a href="http://www.asptoday.com/articles/20000822.htm">http://www.asptoday.com/articles/20000822.htm</a> , 10/10/00, pgs. 1-5
I	C25	"Network Processor Units: The Rising Storm" <a href="http://www.sunworld.com/sunworldonline/swol-07-2000/f_swol-0700-npu.html">http://www.sunworld.com/sunworldonline/swol-07-2000/f_swol-0700-npu.html</a> , 8/30/00, pgs. 1-6
I	C26	Russell Johnson, "Routing Depends on Switch Fabric" <a href="http://www.eetimes.com/printableArticle?doc_id=OEG20000124S0043">http://www.eetimes.com/printableArticle?doc_id=OEG20000124S0043</a> , 9/19/00, pgs. 1-4
I	C27	Matsumoto and et al., "Startups Stake Out Turf in Network Processor Battle" <a href="http://www.eetimes.com/story/industry/semiconductor_news/OEG19990913S0028">http://www.eetimes.com/story/industry/semiconductor_news/OEG19990913S0028</a> , 9/11/00 pgs. 1-7

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Information Disclosure Statement — PTO-1449 (Modified)

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App. Docket No.

SURG:158

Serial No.

09/871,134

Applicant

Scott C. Johnson, et al.

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U.S. Patent Documents

See Page

Foreign Patent Documents

See Page

Other Art

See Page 1

## U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A1						

## Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						

## Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
✓	C41	"TrueSan FusionOS™ Software: The Information Renaissance: Unifying Storage Applications and Transports", TrueSan Networks, Inc., 2001, pgs. 1-11.
	C42	"Increasing Intelligence Within The San Fabric", Brocade, White Paper, June 2001, pgs. 1-6.
	C43	"The Essential Elements Of A Storage Networking Architecture", Brocade, White Paper, June 2001, pgs. 1-10.
	C44	"Brocade Fabric OS For 2 Gbit/sec Switches", Brocade, October 2001, 4 pgs.
	C45	"System Level Reference Design For IEEE 1355 Packet Routing Switch", Xilinx, December 7, 2001, pgs. 1-5.
	C46	BigIron Architecture Technical Brief, White Paper, Foundary Networks BigIron, Version 3.2, December 2001, pgs. 1-16.
	C47	Data Sheet, Cisco 12410 Internet Router, April 23, 2001, 12 pgs.
	C48	"Virtual Interface Architecture Specification", Compaq Computer Corporation, Intel Corporation and Microsoft Corporation, Version 1.0, December 16, 1997, pgs. 1-83.
✓	C49	"ATM" Computer Desktop Encyclopedia, <a href="http://www.techweb.com/encyclopedia/defineterm?term=atm">http://www.techweb.com/encyclopedia/defineterm?term=atm</a> , 1/18/02, pgs. 1-5

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Serial No.

09/871,134

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Scott C. Johnson et al.

Filing Date:

May 31, 2001

Group:

2152

U.S. Patent Documents

See Page

Foreign Patent Documents

See Page

Other Art

See Pages 1-2

**U.S. Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A1						

**Foreign Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						

**Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)**

Exam. Init.	Ref. Des.	Citation
ND	C28	Braden, Ed., et al., "Resource ReSerVation Protocol (RSVP)", Version 1 Functional Specification, Sept. 1997
	C29	Blake, et al., "An Architecture for Differentiated Services", Dec. 1998
	C30	Nichols, et al., "Definition of the Differentiated Services Field (DS Field) in the Ipv4 and Ipv6 Headers", Dec. 1998
	C31	"White Paper-Introduction to QoS Policies", Stardust.com, Inc., July 6, 1999
	C32	"White Paper-QoS Protocols & Architectures", Stardust.com, Inc., July 8, 1999
	C33	"Technology Backgrounder-Quality of Service-Glossary of Terms", Compiled by Vicki Johnson, Stardust.com, Inc., May 1999
	C34	Platt, Charles., "The Future Will Be Fast But Not Free", May 2001
	C35	Copending U.S. Application Serial No. 09/871,401, Filed May 31, 2001, "Network Connected Computing System Including Network Switch"
	C36	Copending U.S. Application Serial No. 09/797,413, Filed March 1, "Network Connected Computing System"
	C37	"Quality of Service (QoS) in the New Public Network Architecture", <i>IEEE Canadian Review</i> - Fall/Automne 2000

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Atty. Docket No.

SURG:158

Serial No.

09/871,134

Applicant

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Filing Date:

May 31, 2001

Group:

2152

U.S. Patent Documents

See Page

Foreign Patent Documents

See Page

Other Art

See Pages 1-2

## Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
<i>W</i>	C38	Armitage, Grenville et al., "MPLS: The Magic Behind the Myths", <i>IEEE Communications Magazine</i> , January 2000
<i>J</i>	C39	InfiniBand™ Architecture Specification Volume 1 - Release 1.0, InfiniBand™ Trade Association, October 24, 2000
<i>J</i>	C40	InfiniBand™ Architecture Specification Volume 2 - Release 1.0, InfiniBand™ Trade Association, October 24, 2000

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